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PATENT

Docket No. JCLA11354

Date: 9-4-2003

Page 1

ASSISTANT COMMISSIONER FOR PATENTS
P.O. BOX 1450, ALEXANDRIA, VA 22313-1450
ATTENTION: APPLICATION BRANCH

Sir:

Transmitted herewith for filing is the patent application of
Inventors: Wun Wang

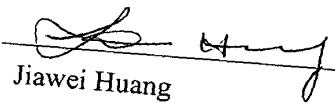
For: SYSTEM AND METHOD FOR EFFECTIVE YIELD LOSS ANALYSIS FOR
SEMICONDUCTOR WAFERS

Enclosed are:

- Specification 19 pages.
- 6 Sheets of drawings
- Recordation Form Cover sheet with 2 pages assignment.
- A certified copy of Taiwan Patent Application No. dated .
- SIGNED** declaration and power of attorney.
- Return Prepaid postcard.

FOR	CLAIMS AS FILED				
	NUMBER FILED	NUMBER EXTRA	RATE	FEE	
Basic Fee				\$750.00	\$750.00
Total Claims	10	—	20 = 0	×	\$18.00 \$0.00
Independent Claims	1	—	3 = 0	×	\$84.00 \$0.00
If application contains any multiple dependent claim(s), then add				\$280.00	\$0.00
	TOTAL FILING FEE				<u>\$750.00</u>

- A check in the amount of \$750.00 cover the filing fee is enclosed.
- A check in the amount of \$40.00 to cover the assignment recording fee.
- A duplicate copy of this sheet is enclosed.


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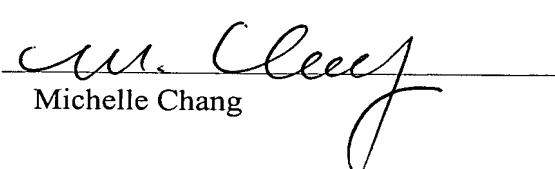
Docket No. : JCLA11354
Applicant(s) : Wun Wang
For : SYSTEM AND METHOD FOR EFFECTIVE
YIELD LOSS ANALYSIS FOR
SEMICONDUCTOR WAFERS
“Express Mail” : EV 338303962 US
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Date of Deposit : *September 4, 2003*

I hereby certify that the accompanying

Transmittal in Duplicate; Specification 19 pages, 6 sheets of drawings; **SIGNED**
Declaration and Power of Attorney 2 pages; Recordation Form Cover Sheet and
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Michelle Chang